ABSTRACT OF DISCLOSURE

In a headlamp apparatus comprising a sub-CPU integrally provided on a headlamp for controlling an optical axis direction changing operation and a main CPU for sending out to the sub-control circuit a control signal for changing the direction of the optical axis, the sub-CPU comprises a power-on resetting circuit of a simple configuration comprising a capacitor and a resistor. When the main CPU detects an abnormality in the sub-CPU, a power supply is temporarily cut off by a power supply control means, and the sub-CPU is reset by a power-on resetting circuit. An abnormality triggered by a runaway of the sub-CPU can be resolved so that the sub-CPU can be restored to a normal condition as quickly as possible.

15 [Selected Figure] Fig. 7

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